step00 br(if notEXEC then step00)

step01 br(case(HALT, RTS, RTI, ROR, RORC, PUSHPSW, POPPSW, STIVTP, BGREU, BLSSU, BLEQU, JGRT, JLEQ, JMP, JSR, LD, ST, ADD, DEC, OR, STRFIND)

then (HALT, step3D), (RTS, step22), (RTI, step1A), (ROR, step2F), (RORC, step31), (PUSHPSW, step2A),(POPPSW, step1A), (STIVTP, step33), (BGREU, step11), (BLSSU, step11), (BLEQU, step11), (JGRT, step13), (JLEQ, step13), (JMP, step14), (JSR, step15), (LD, step02), (ST, step04), (ADD, step0B), (DEC, step0F), (OR, step0D), (STRFIND, step34))

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//-------------LD-------------

step02 ldA

step03 ldN, ldZ, br step3E

//------------ST-------------

step04 br(if regdir then step0A)

step05 mxMDR0, ldMDR

step06 wrMEM, br(if notFCBUS then step06)

step07 mxMDR1, ldMDR, incMAR

step08 wrMEM, br(if notFCBUS then step08)

step09 br step3E

step0A mxGPR0, wrGPR, br step3E

//---------ADD---------------

step0B mxA0, ldA, ldV, ldC

step0C ldN, ldZ, br step3E

//-------------OR---------------

step0D mxA0, mxA2, ldA

step0E ldN, ldZ, br step3E

//------------DEC-------------

step0F mxA1, ldA, ldV, ldC

step10 ldN, ldZ, br step3E

//-----------BGREU, BLSSU, BLEQU------------

step11 br(if notbrorjmp then step3E)

step12 mxPC0, ldPC, br step3E

//----------JGRT, JLEQ---------------

step 13 br(if notbrorjmp then step3E)

//----------JMP---------------------

step14 ldPC, br step3E

//---------JSR-----------------------

step15 mxMAR2, ldMAR, decSP, mxMDR2, ldMDR

step16 wrMEM, br(if notFCBUS then step16)

step17 mxMAR2, ldMAR, decSP, mxMDR0, mxMDR1, ldMDR

step18 wrMEM, br(if notFCBUS then step18)

step19 ldPC, br step3E

//--------POPPSW, RTI---------------

step1A incSP

step1B mxMAR2, ldMAR

step1C rdMEM, ldMDR, br(if notFCBUS then step1C)

step1D ldI

step1E incSP

step1F mxMAR2, ldMAR

step20 rdMEM, ldMDR, br(if notFCBUS then step20)

step21 ldC, ldV, ldN, ldZ, stPSWSTART, br(if notbrinsttype then step3E)

//---------------RTS---------------------

step22 incSP

step23 mxMAR2, ldMAR

step24 rdMEM, ldMDR, br(if notFCBUS then step24)

step25 ldDW15..8, incSP

step26 mxMAR2, ldMAR

step27 rdMEM, ldMDR, br(if notFCBUS then step27)

step28 ldDW7..0

step29 mxPC1, ldPC, br step3E

//-----------PUSHPSW------------------

step2A mxMAR2, ldMAR, decSP, mxMDR0, mxMDR2, ldMDR

step2B wrMEM, br(if notFCBUS then step2B)

step2C mxMAR2, ldMAR, decSP, mxMDR1, mxMDR2, ldMDR

step2D wrMEM, br(if notFCBUS then step2D)

step2E br step3E

//-----------------ROR-----------------

step2F mxA0, mxA1, ldA

step30 ldN, ldZ, br step3E

//---------------RORC--------------

step31 mxA2, ldA, ldC

step32 ldN, ldZ, br step3E

//-----------STIVTP--------------

step33 ldIVTP, br step3E

//---------STRFIND------------

step34 clSTRCNT, mxMAR0, mxMAR1, ldMAR

step35 rdMEM, ldMDR, br(if notFCBUS then step35)

step36 br(if notENDOFARRAY then step39)

step37 mxA1, mxA2, ldA

step38 ldN, br step3E

step39 br(if notACCEQMDR then step3C)

step3A mxA0, mxA1, mxA2, ldA

step3B ldZ, br step3E

step3C incSTRCNT, incMAR, br step35

//-----------HALT---------------

step3D clPSWSTART

//--------------------------------

step3E stINTR, clEXEC, br step00